

## PATENT ABSTRACTS OF JAPAN

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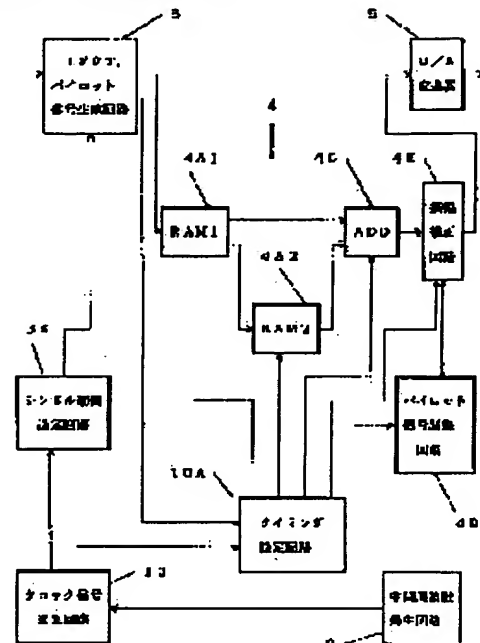
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## (54) ORTHOGONAL FREQUENCY DIVISION MULTIPLEX SIGNAL TRANSMITTER

## (57) Abstract:

**PURPOSE:** To reduce the interference distortion of spurious frequency components due to the signal waveform of a connection point by providing a signal insertion period between symbols in an OFDM signal transmitter.

**CONSTITUTION:** This transmitter is provided with an IFFT and pilot signal generation circuit 3 for being supplied with digital information signals and generating multi-valued QAM modulation signals, a guard interval and connection signal period setting circuit 4 constituted to repeatedly transmitting a part of modulation signals for prescribed time and a clock signal generation circuit 10 for driving both circuits. By the guard interval and connection signal period setting circuit 4, a symbol period is divided into a connection signal period, a guard interval and a valid symbol period, the signals of the respective periods are set and the signals for suppressing the generation of distortion components are inserted to the connection signal period.



## LEGAL STATUS

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